

6.002

**CIRCUITS AND
ELECTRONICS**

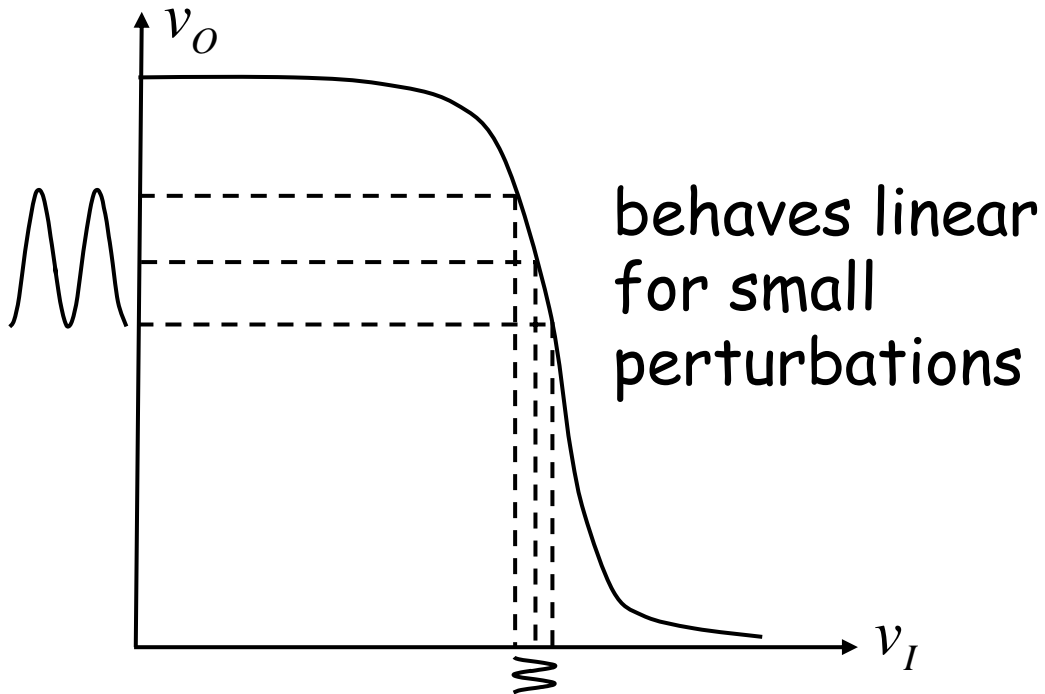
Small Signal Circuits

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6.002 Fall 2000 Lecture 11

Review:

I Graphical view (using transfer function)



Review:

II Mathematical view

$$v_o = V_s - \frac{K(v_I - V_T)^2}{2} R_L$$

$$v_o = \frac{d}{dv_I} \left[V_s - \frac{K}{2} (v_I - V_T)^2 R_L \right] \cdot v_i$$

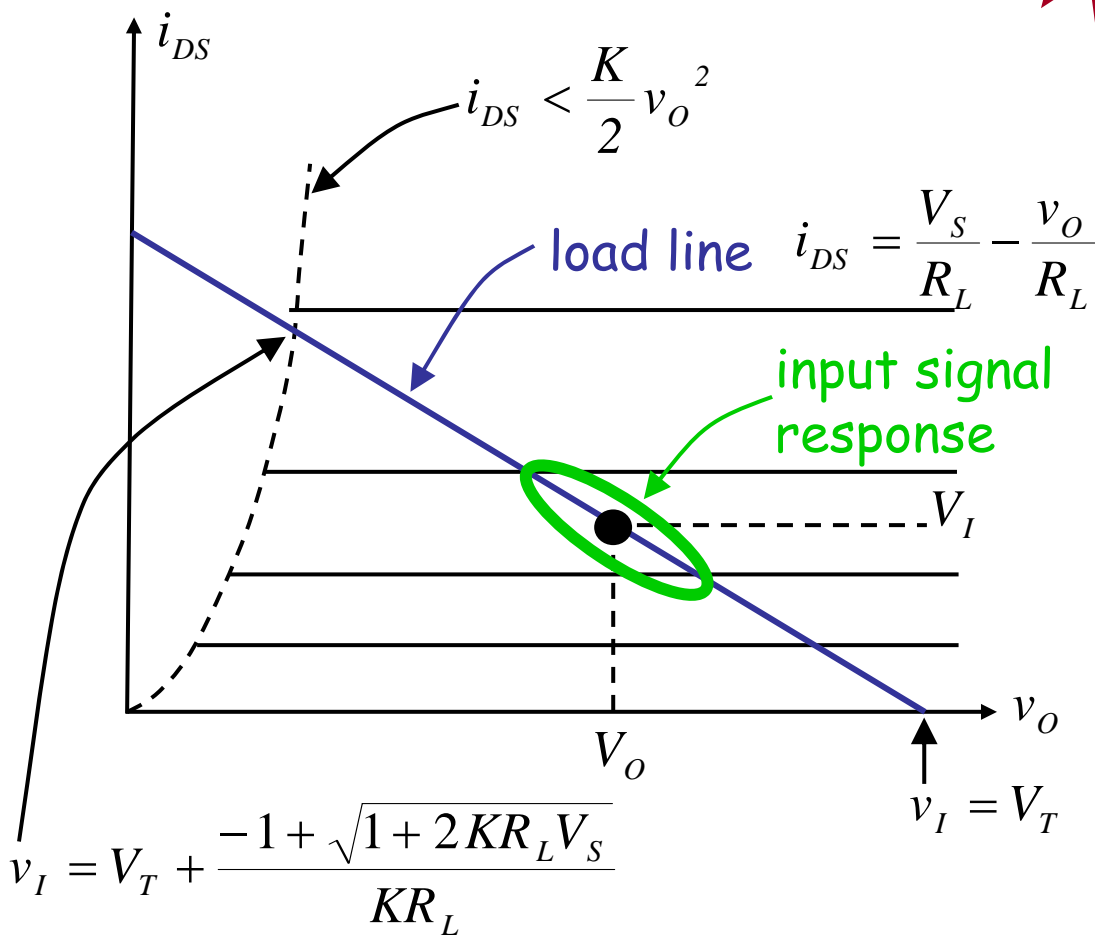
$v_I = V_I$

$$v_o = - \underbrace{K(V_I - V_T)}_{g_m} R_L \cdot v_i$$

g_m

related to V_I
constant for fixed
DC bias

How to choose the bias point, using yet another graphical view based on the load line



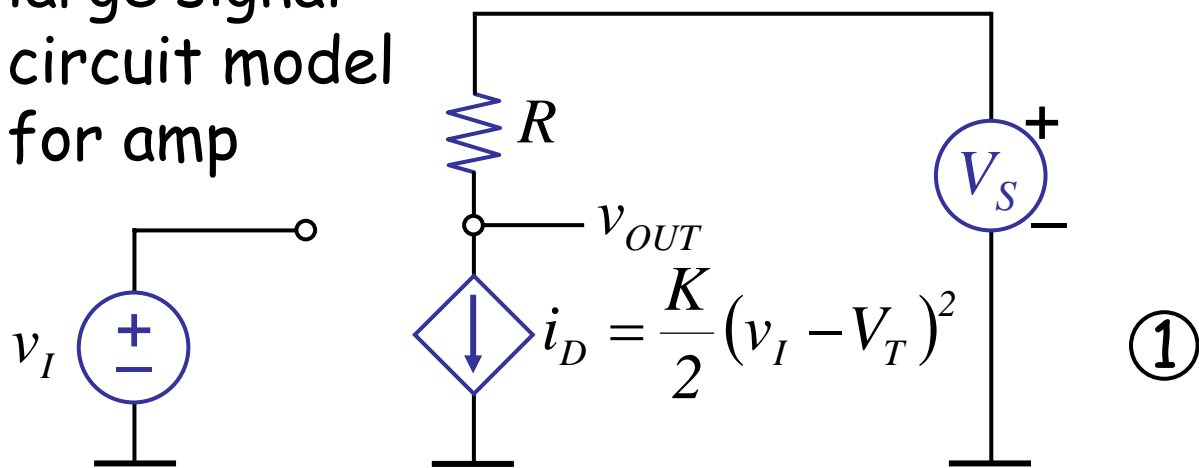
Choosing a bias point:

1. Gain $g_m R_L \propto V_I$
2. Input valid operating range for amp.
3. Bias to select gain and input swing.

III The Small Signal Circuit View

We can derive small circuit equivalent models for our devices, and thereby conduct small signal analysis directly on circuits

e.g. large signal circuit model for amp



We can replace large signal models with small signal circuit models.

Foundations: Section 8.2.1 and also in the last slide in this lecture.

Small Signal Circuit Analysis

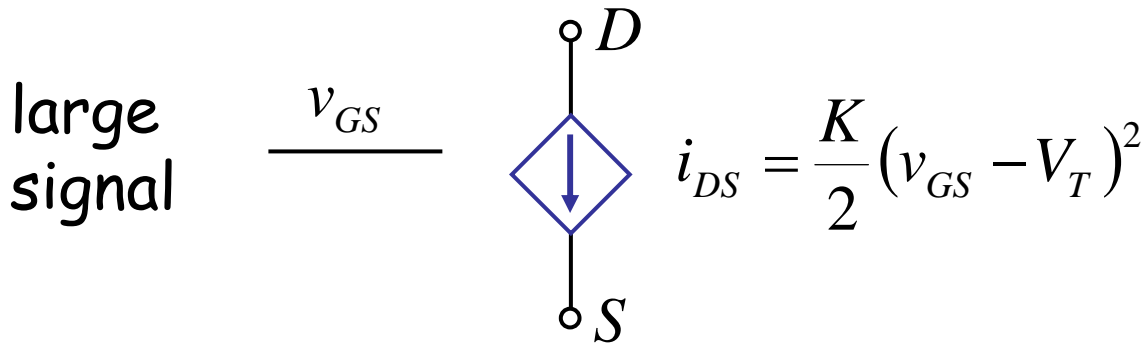
- ① Find operating point using DC bias inputs using large signal model.
- ② Develop small signal (linearized) models for elements.
- ③ Replace original elements with small signal models.

Analyze resulting linearized circuit...

Key: Can use superposition and other linear circuit tools with linearized circuit!

Small Signal Models

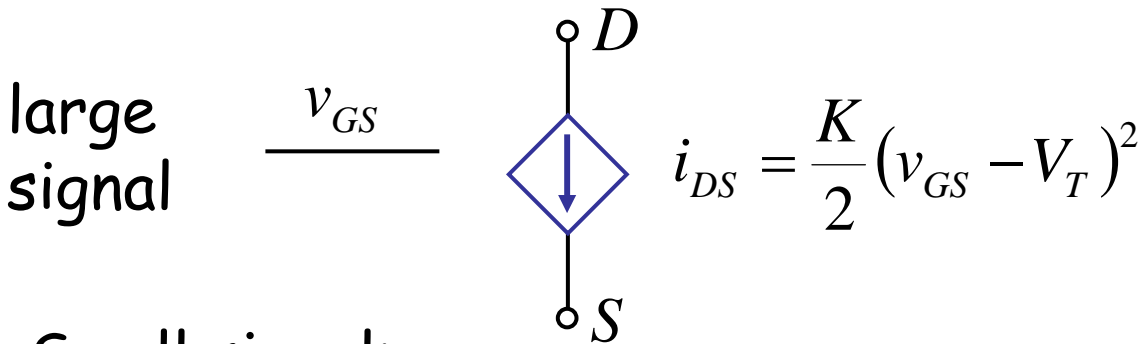
Ⓐ MOSFET



Small signal?

Small Signal Models

(A) MOSFET

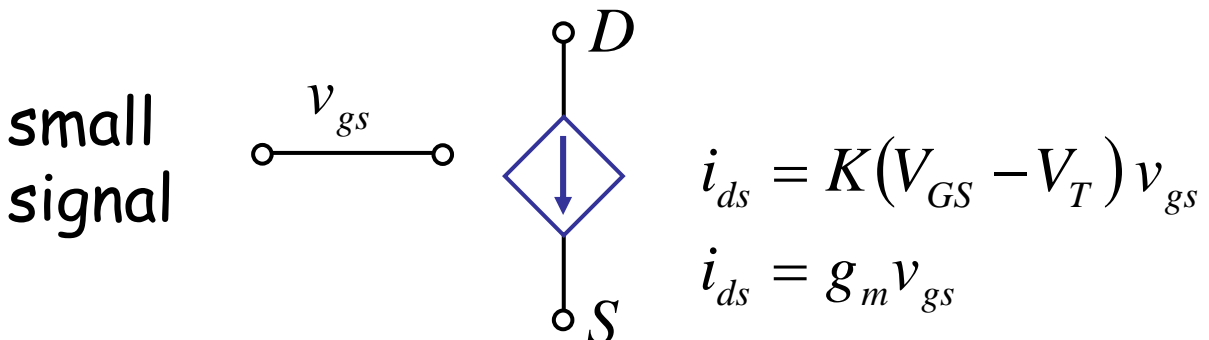


Small signal:

$$i_{DS} = \frac{K}{2} (v_{GS} - V_T)^2$$

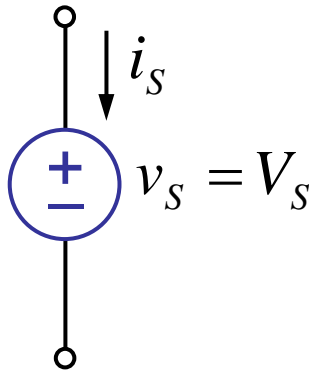
$$i_{ds} = \frac{\partial}{\partial v_{GS}} \left[\frac{K}{2} (v_{GS} - V_T)^2 \right] \Big|_{v_{GS} = V_{GS}} \cdot v_{gs}$$

$$i_{ds} = \underbrace{K (V_{GS} - V_T)}_{g_m} \cdot v_{gs} \quad \Rightarrow \quad i_{ds} \text{ is linear in } v_{gs}!$$



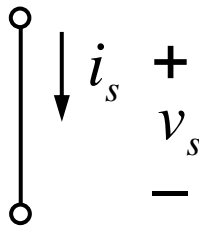
Ⓑ DC Supply V_S

large
signal



$$v_S = V_S$$

Small signal



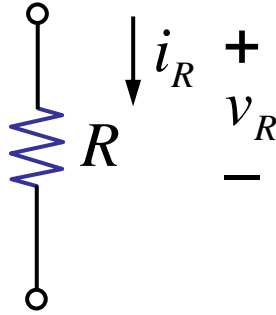
$$v_s = \left. \frac{\partial V_S}{\partial i_S} \right|_{i_S = I_S} \cdot i_s$$

$$v_s = 0$$

DC source behaves
as short to small
signals.

③ Similarly, R

large
signal

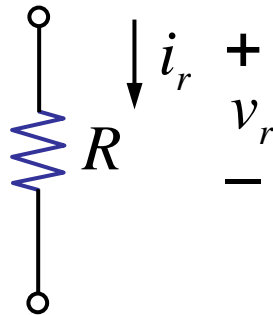


$$v_R = R i_R$$

$$v_r = \left. \frac{\partial(Ri_R)}{\partial i_R} \right|_{i_R=I_R} \cdot i_r$$

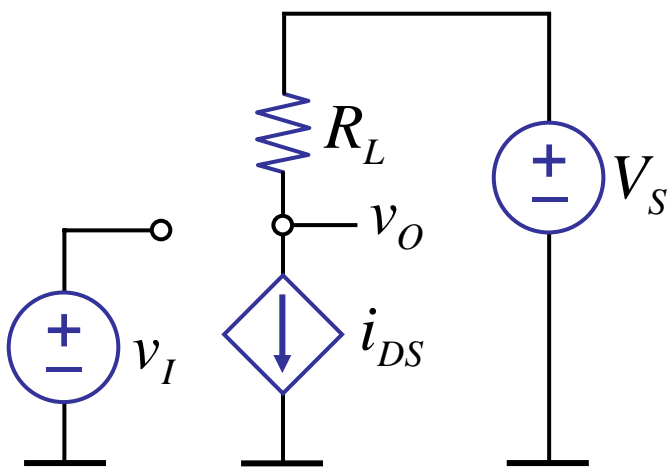
$$v_r = R \cdot i_r$$

small
signal



Amplifier example:

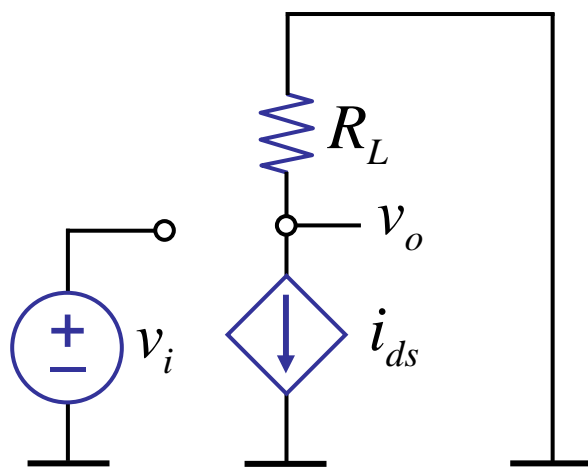
Large signal



$$i_{DS} = \frac{K}{2} (v_I - V_T)^2$$

$$v_O = V_S - \frac{K}{2} (v_I - V_T)^2 R_L$$

Small signal



$$i_{ds} = K(V_I - V_T) \cdot v_i$$

$$i_{ds} R_L + v_o = 0$$

$$v_o = -i_{ds} R_L$$

$$v_o = -K(V_I - V_T) R_L \cdot v_i$$

$$= -g_m R_L \cdot v_i$$

Notice, first we need to find operating point voltages/currents.

Get these from a large signal analysis.

III The Small Signal Circuit View

To find the relationship between the small signal parameters of a circuit, we can replace large signal device models with corresponding small signal device models, and then analyze the resulting small signal circuit.

Foundations: (Also see section 8.2.1 of A&L)

KVL, KCL applied to some circuit C yields:

$$\dots + v_A + \dots + v_{OUT} + \dots + v_B + \dots$$

1

Replace total variables with operating point variables plus small signal variables

$$\dots + V_A + v_a \dots + V_{OUT} + v_{out} + V_B + v_b + \dots$$

Operating point variables themselves satisfy the same KVL, KCL equations

$$\dots + V_A \dots + V_{OUT} + V_B + \dots$$

so, we can cancel them out

Leaving

$$\dots + v_a \dots + v_{out} + v_b + \dots$$

2

But 2 is the same equation as 1 with small signal variables replacing total variables, so 2 must reflect same topology as in C , except that small signal models are used.

Since small signal models are linear, our linear tools will now apply...